

THS4130IDGN

SP Amp DIFF AMP Single ±16.5V/33V 8-Pin HVSSOP EP Tube

| Manufacturer: | Texas Instruments, Inc. |
|---------------|----------------------------|
| Package/Case: | MSOP8 |
| Product Type: | Amplifier ICs |
| RoHS: | RoHS Compliant/Lead free W |
| Lifecycle: | Active |



Images are for reference only

General Description

The SN65MLVD047A is a quadruple line driver that complies with the TIA/EIA-899 standard, Electrical Characteristics of Multipoint-Low-Voltage Differential Signaling (M-LVDS). The output current of this M-LVDS device has been increased, in comparison to standard LVDS compliant devices, in order to support doubly terminated transmission lines and heavily loaded backplane bus applications. Backplane applications generally require impedance matching termination resistors at both ends of the bus. The effective impedance of a doubly terminated bus can be as low as 30 **Ω**. The SN65MLVD047A devices allow for multiple drivers to be present on a single bus. SN65MLVD047A drivers are high impedance when disabled or unpowered. Driver edge rate control is incorporated to support operation. The M-LVDS standard allows up to 32 nodes (drivers and/or receivers) to be connected to the same media in a backplane when multiple bus stubs are expected from the main transmission line to interface devices. The SN65MLVD047A provides 9-kV ESD protection on all bus pins.

Key Features



Differential Line Drivers for 30-

Loads and Data Rates(1) Up to 200 Mbps, Clock Frequencies up to 100 MHz

Supports Multipoint Bus Architectures

Meets the Requirements of TIA/EIA-899

Operates from a Single 3.3-V Supply

Characterized for Operation from -40°C to 85°C

16-Pin SOIC (JEDEC MS-012) and 16-Pin TSSOP (JEDEC MS-153) Packaging

APPLICATIONS AdvancedTCA (ATCA) Clock Bus Driver

Clock Distribution

Backplane or Cabled Multipoint Data Transmission in Telecommunications, Automotive, Industrial, and Other Computer Systems

Cellular Base Stations

Central-Office and PBX Switching

Bridges and Routers

Low-Power High-Speed Short-Reach Alternative to TIA/EIA-485

(1) The data rate of a line, is the number of voltage transitions that are made per second expressed in the units bps (bits per second).

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Recommended For You

THS3092D Texas Instruments, Inc SOP-8 THS7316DR

Texas Instruments, Inc SOP-8 THS41311DGNR

Texas Instruments, Inc MSOP8

THS4011CD

Texas Instruments, Inc SOP

THS4503IDGN

Texas Instruments, Inc

MSOP8

THS4130IDGK

Texas Instruments, Inc

MSOP8

THS4281D Texas Instruments, Inc

SOIC-8

THS7374IPW

Texas Instruments, Inc TSSOP14

THS7376IPWR

Texas Instruments, Inc TSSOP14

THS7353PW Texas Instruments, Inc TSSOP20

THS4631D Texas Instruments, Inc SOP-8

THS6184RHFR

Texas Instruments, Inc QFN

THS7314D

Texas Instruments, Inc SOP8

THS4551IRGIR

Texas Instruments, Inc VQFN16

THS3061DGN

Texas Instruments, Inc MSOP8