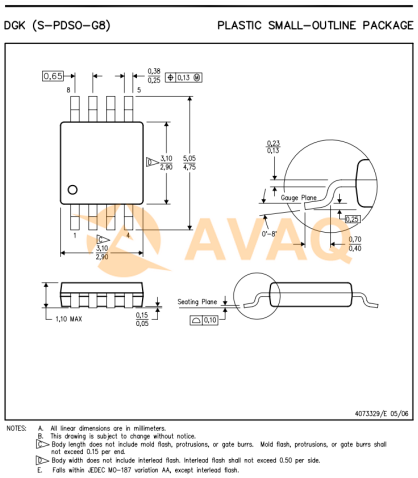
LMV762MMX/NOPB

Comparator Dual 5.25V Automotive 8-Pin VSSOP T/R

Manufacturer:	Texas Instruments, Inc
Package/Case:	MSOP8
Product Type:	Linear Displacement Sensors
RoHS:	RoHS Compliant/Lead free
Lifecycle:	Active



Images are for reference only

Inquiry

General Description

The LMV76x devices are precision comparators intended for applications requiring low noise and low input offset voltage. The LMV761 single has a shutdown pin that can be used to disable the device and reduce the supply current. The LMV761 is available in a space-saving 6-pin SOT-23 or 8-Pin SOIC package. The LMV762 dual is available in 8-pin SOIC or VSSOP package. The LMV762Q-Q1 is available VSSOP and SOIC packages.
These devices feature a CMOS input and push-pull output stage. The push-pull output stage eliminates the need for an external pullup resistor.
The LMV76x are designed to meet the demands of small size, low power and high performance required by portable and battery-operated electronics.
The input offset voltage has a typical value of 200 µV at room temperature and a 1-mV limit over temperature.

Key Features

V_S = 5 V, T_A = 25°C, Typical Values Unless Specified

Input Offset Voltage 0.2 mV

Input Offset Voltage (Maximum Over Temp) 1 mV

Input Bias Current 0.2 pA

Propagation Delay (OD = 50 mV) 120 ns

Low Supply Current 300 µA

CMRR 100 dB

PSRR 110 dB

Extended Temperature Range 40°C to +125°C

Push-Pull Output

Ideal for 2.7-V and 5-V Single-Supply Applications

Available in Space-Saving Packages:
6-Pin SOT-23 (Single With Shutdown)

8-Pin SOIC (Single With Shutdown)

8-Pin SOIC and VSSOP (Dual Without Shutdown)

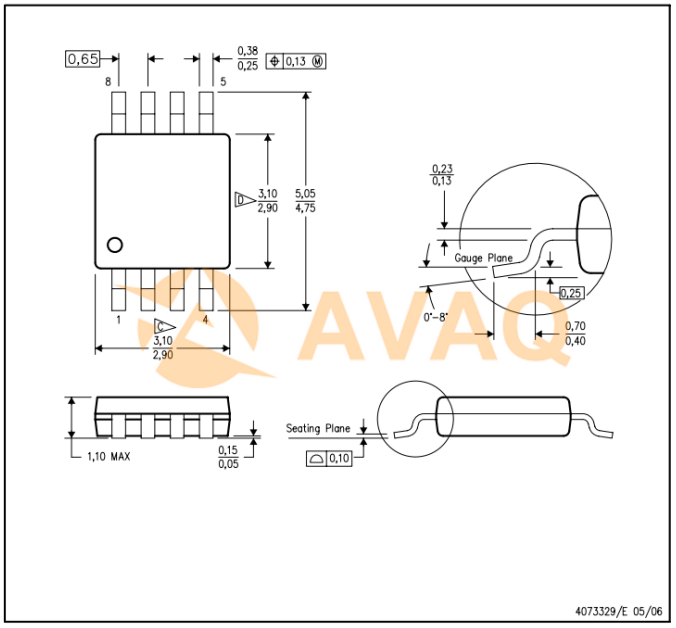
LMV762Q-Q1 is Qualified for Automotive Applications

AEC-Q100 Qualified With the Following Results:
Device Temperature Grade 1: –40°C to +125°C Ambient Operating Temperature Range

Device HBM ESD Classification Level 1C

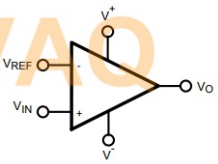
Device CDM ESD Classification Level M2

DGK (S–PDSO–G8) PLASTIC SMALL–OUTLINE PACKAGE



NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 per end.
D. Body width does not include interlead flash. Interlead flash shall not exceed 0.50 per side.
E. Falls within JEDEC MO–187 variation AA, except interlead flash.

7.2 Functional Block Diagram



Recommended For You

LM311MX
Texas Instruments, Inc
SOP8

LMV7219M5
Texas Instruments, Inc
SOT23-5

LM348D
Texas Instruments, Inc
SOP-14

LM224N
Texas Instruments, Inc
DIP14

LM239J
Texas Instruments, Inc
CDIP14

LMV331M5
Texas Instruments, Inc
SOT23-5

LM393ADR
Texas Instruments, Inc
SOP8

LM293DR
Texas Instruments, Inc
SOP8

LM293D
Texas Instruments, Inc
SOP8

LMV824MTX
Texas Instruments, Inc
TSSOP

LMV358M
Texas Instruments, Inc
SOP8

LMV321M5
Texas Instruments, Inc
SOT23-5

LM741H
Texas Instruments, Inc
CAN8

LM193AH
Texas Instruments, Inc
CAN8

LM111H/NOPB
Texas Instruments, Inc
CAN8