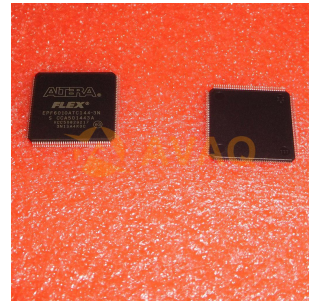


FPGA FLEX 6000 Family 10K Gates 880 Cells 142.86MHz 0.42um
Technology 3.3V 144-Pin TQFP



Images are for reference only

Manufacturer: [Intel Corp](#)

Package/Case: TQFP

Product Type: Programmable Logic ICs

RoHS: RoHS Compliant/Lead free 

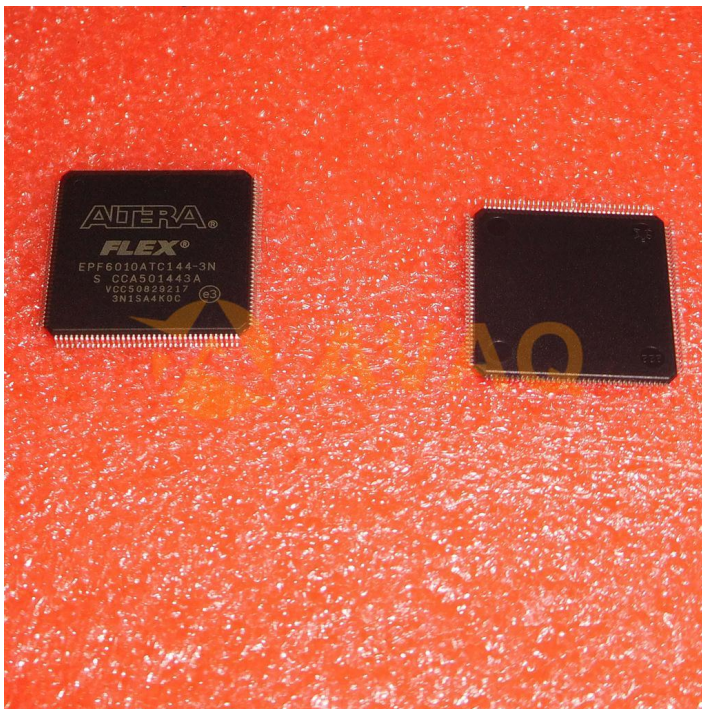
Lifecycle: Obsolete

[Inquiry](#)

General Description

The Altera® FLEX 6000 programmable logic device (PLD) family provides a low-cost alternative to high-volume gate array designs. FLEX 6000 devices are based on the OptiFLEX architecture, which minimizes die size while maintaining high performance and routability. The devices have reconfigurable SRAM elements, which give designers the flexibility to quickly change their designs during prototyping and design testing. Designers can also change functionality during operation via in-circuit reconfiguration.

FLEX 6000 devices are reprogrammable, and they are 100% tested prior to shipment. As a result, designers are not required to generate test vectors for fault coverage purposes, allowing them to focus on simulation and design verification. In addition, the designer does not need to manage inventories of different gate array designs. FLEX 6000 devices are configured on the board for the specific functionality required.



Recommended For You

EPMB256AQC208-10N

Intel Corp

QFP208

EPCQ32ASI8N

Intel Corp

SOP8

EPCQ32SI8N

Intel Corp

SOP8

EPCQ64ASI16N

Intel Corp

SOP16

EPCQ16SI8N

Intel Corp

SOP8

EPC2II32

Intel Corp

QFP

EPM7128STC100-15N

Intel Corp

QFP100

EP1C6Q240I7N

Intel Corp

QFP240

EPCQ128SI16N

Intel Corp

SOP16

EPM7128SLC84-15N

Intel Corp

PLCC

EPC1213PC8

Intel Corp

DIP8

EP1K30TC144-3N

Intel Corp

QFP

EPCS1S18

Intel Corp

SOP-8

EPC1PI8N

Intel Corp

DIP8

EPC2LI20N

Intel Corp

PLCC