

**FPGA Configuration PROM Parallel/Serial 512K-bit 2.5V/3.3V  
20-Pin SOIC**



Images are for reference only

[Inquiry](#)

<b>Manufacturer:</b>	<a href="#">AMD Xilinx, Inc</a>
<b>Package/Case:</b>	SOP20
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete

### General Description

XC18V512SO20C is a specific model of a programmable read-only memory (PROM) chip produced by Xilinx, a leading manufacturer of programmable logic devices. The chip belongs to the XC18V family and has a capacity of 512K bits (64K x 8 bits) of memory.

### Key Features

- It operates on a single power supply of 3.3V.
- It has a fast access time of 15 ns, which means that data can be read from the memory in 15 nanoseconds.
- It is a non-volatile memory, which means that the data stored in the chip is retained even when the power is turned off.
- It has a small form factor package with a SOIC-20 footprint.

### Application

- Embedded systems:** The chip can be used as a boot memory for microcontrollers and digital signal processors (DSPs) in various embedded systems.
- Automotive electronics:** It can be used in automotive applications that require high reliability and robustness.
- Industrial control systems:** It can be used in various industrial control systems such as factory automation and robotics.

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