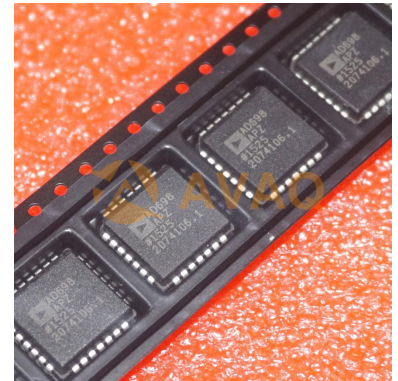


Sensor and Detector Interface 36V 4-Wire Interface 28-Pin PLCC Tube



Images are for reference only

[Inquiry](#)

Manufacturer: [Analog Devices, Inc](#)

Package/Case: PLCC28

Product Type: Drivers

RoHS: RoHS Compliant/Lead free 

Lifecycle: Active

General Description

The AD698 is a complete, monolithic Linear Variable Differential Transformer (LVDT) signal conditioning subsystem. It is used in conjunction with LVDTs to convert transducer mechanical position to a unipolar or bipolar dc voltage with a high degree of accuracy and repeatability. All circuit functions are included on the chip. With the addition of a few external passive components to set frequency and gain, the AD698 converts the raw LVDT output to a scaled dc signal. The device will operate with half-bridge LVDTs, LVDTs connected in the series opposed configuration (4-wire), and RVDTs.

The AD698 contains a low distortion sine wave oscillator to drive the LVDT primary. Two synchronous demodulation channels of the AD698 are used to detect primary and secondary amplitude. The part divides the output of the secondary by the amplitude of the primary and multiplies by a scale factor. This eliminates scale factor errors due to drift in the amplitude of the primary drive, improving temperature performance and stability.

The AD698 uses a unique ratiometric architecture to eliminate several of the disadvantages associated with traditional approaches to LVDT interfacing. The benefits of this new circuit are: no adjustments are necessary; temperature stability is improved; and transducer interchangeability is improved.

The AD698 is available in two performance grades:

Grade Temperature Range Package AD698AP -40°C to +85°C 28-Pin PLCC AD698SQ -55°C to +125°C 24-Pin Cerdip

Key Features

Single chip solution, contains internal oscillator and voltage reference

No adjustments required

DC output proportional to position

20Hz to 20kHz Frequency range

Unipolar or bipolar output

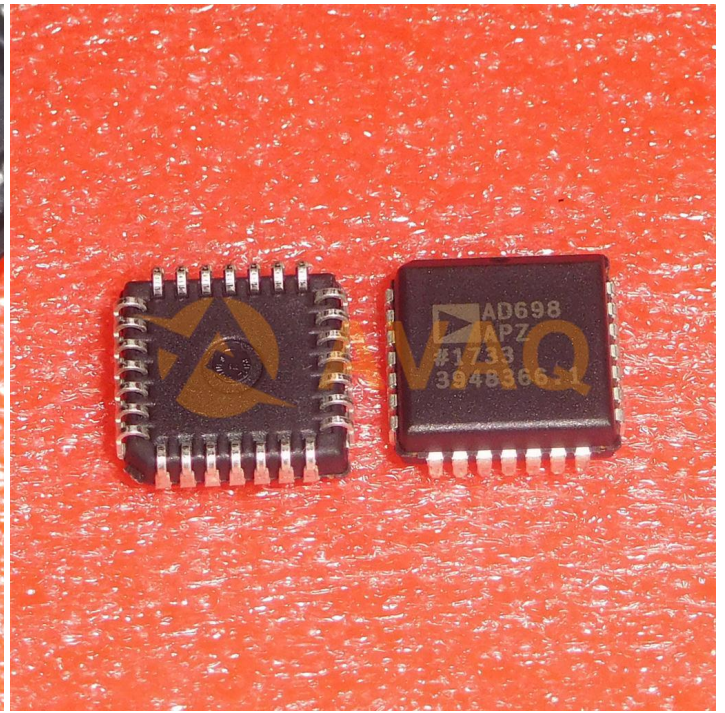
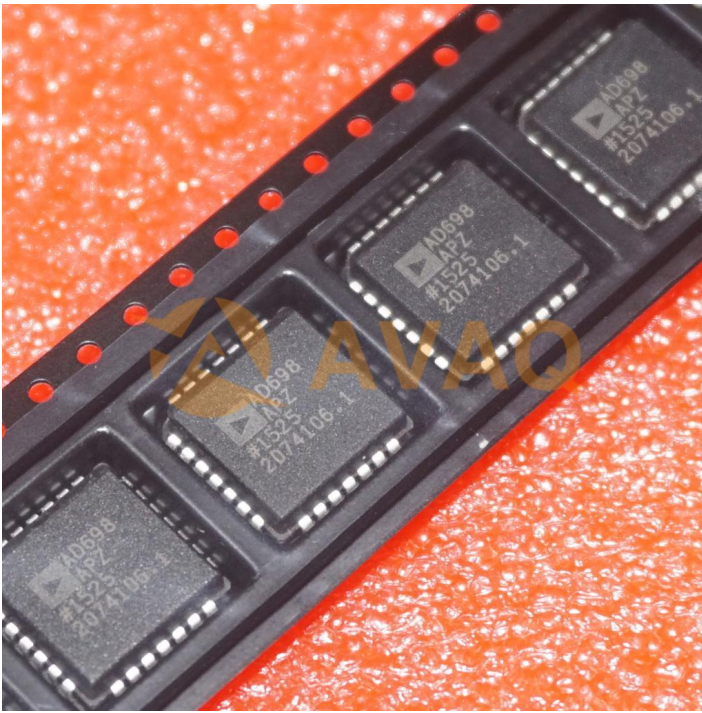
Decodes AC bridge signals

Outstanding performance

0.05% Linearity

20ppm/°C Typical gain drift

5ppm/°C Typical offset drift



Recommended For You

ADM3490EARZ

Analog Devices, Inc

SOP-8

ADuM3160BRWZ-RL

Analog Devices, Inc

SOP16

ADM3232EARUZ

Analog Devices, Inc

TSSOP-16

ADuM5211ARSZ

Analog Devices, Inc

SSOP20

ADuMI201BRZ-RL7

Analog Devices, Inc

SOP8

ADV7623BSTZ

Analog Devices, Inc

LQFP144

ADuMI410BRWZ

Analog Devices, Inc

SOP16

ADM3251EARWZ

Analog Devices, Inc

SOP20

ADM485ANZ

Analog Devices, Inc

DIP

ADuM6400ARWZ

Analog Devices, Inc

SOP16

ADuMI281BRZ

Analog Devices, Inc

SOP8

ADUM142E0BRZ

Analog Devices, Inc

SOP-16

ADuMI412BRWZ

Analog Devices, Inc

SOP16

ADV7622BSTZ

Analog Devices, Inc

TQFP144

ADAU1328BSTZ

Analog Devices, Inc

QFP