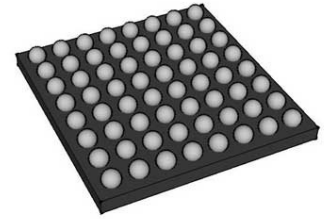



**RF Receiver AM/FM 1.2V/1.8V/3.3V Automotive 168-Pin  
LFBGA Tray**

Images are for reference only

[Inquiry](#)

<b>Manufacturer:</b>	<a href="#">STMicroelectronics, Inc</a>
<b>Package/Case:</b>	BGA
<b>Product Type:</b>	Communication & Networking ICs
<b>RoHS:</b>	RoHS Compliant/Lead free 
<b>Lifecycle:</b>	Active

**General Description**

The STA680 is an HD-radio base-band processor for car-radio applications. The STA680 functionality includes audio decompression and data processing, while multiple interfaces ensure flexible integration into the system. The STA680 takes full advantage of HD 1.5 Radio benefits including CD-like audio quality from HD Radio FM broadcasts and FM-like audio quality using HD Radio AM, while program associated data or traffic information is received from the second channel.

## Key Features

AEC-Q100 qualified

IBOC (in-band on-channel) digital audio broadcast signal decoding for AM/FM hybrid and all-digital modes

Dual-channel HD 1.5 for background scanning and data services

HD codec (HDC) audio decompression

Metadata support for HD Radio reception

MPS (main program service), SPS (supplemental program service) and PAD (program associated data) data decoding

Advanced HD Radio feature support:

Apple ID3 tag

Multicasting

Electronic program guide (EPG)

Real-time traffic

Apple ID3 tag

Multicasting

Electronic program guide (EPG)

Real-time traffic

Automatic Audio Alignment (AAA) algorithm support

Variable input base-band data-rate I2S-like interface supporting 650, 675, 744.1875, 912 kS/s data rates

Secondary RF base-band interface for dual tuner applications

Glueless interface to Synchronous SDRAM addressing up to 512 Mbit of SDRAM in x16 configuration

Optional Serial Flash memory SPI interface for application code storage

IIS serial audio interface with programmable sample rate converter

Primary and secondary serial interfaces for host micro communication based on industry standard IIC and SPI

Several General purpose IOs

One Internal clock oscillator and two internal PLLs

External clock input

1.2 V core supply; 3.3 V I/O supply

## Recommended For You

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### STA540

STMicroelectronics, Inc

ZIP15

### STA339BWTR

STMicroelectronics, Inc

SSOP36

### STA559BW

STMicroelectronics, Inc

SSOP36

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**STPA003OD-4WX**

STMicroelectronics, Inc  
144-LQFP

**STA309A13TR**

STMicroelectronics, Inc  
QFP64

**STA120D**

STMicroelectronics, Inc  
SOP28

**STA308**

STMicroelectronics, Inc  
QFP

**STA333W**

STMicroelectronics, Inc  
SSOP36

**STA516B13TR**

STMicroelectronics, Inc  
HSSOP36

**STABP01D**

STMicroelectronics, Inc  
SOP20

**STA333ML**

STMicroelectronics, Inc  
SSOP36

**STA333BW**

STMicroelectronics, Inc  
SSOP36

**STA321**

STMicroelectronics, Inc  
TQFP64

**STA304A**

STMicroelectronics, Inc  
QFP

**STA333IS**

STMicroelectronics, Inc  
CSP-30