

CPLD MAX® 7000S Family 2.5K Gates 128 Macro Cells 76.9MHz  
CMOS Technology 5V 84-Pin PLCC Tray



Images are for reference only

[Inquiry](#)

<b>Manufacturer:</b>	<a href="#">Intel Corp</a>
<b>Package/Case:</b>	PLCC84
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete

## General Description

EPM7128SLC84-15 is a complex programmable logic device (CPLD) developed by Altera (now owned by Intel).

## Key Features

- 128 macrocells
- 7,680 usable gates
- 1024 logic array blocks (LABs)
- 15ns maximum propagation delay time
- 3.3V operation
- 84-pin PLCC package

## Application

- Digital signal processing (DSP)
- Bus interface control
- Power-up sequencing
- State machine control
- Address decoding
- Glue logic



## Recommended For You

**EPM7128SQC100-15**

Intel Corp

QFP

**EPM7128SLC84-10**

Intel Corp

PLCC84

**EPM7128SQC100-7**

Intel Corp

QFP

**EPM7128EQC100-10**

Intel Corp

QFP

**EPM7128SQC160-10**

Intel Corp

QFP

**EPM7128STC100-15**

Intel Corp

TQFP

**EPM7128EQC160-10**

Intel Corp

QFP160

**EPM7128EQC100-7**

Intel Corp

QFP

**EPM7064QC100-12**

Intel Corp

QFP

**EPM7128EQC160-12**

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**EPM7064QC100-7**

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**EPM7064QI100-15**

Intel Corp

PQFP-100