

Signal Processor 128-Pin MQFP Tray

Manufacturer:	Analog Devices, Inc
Package/Case:	QFP
Product Type:	Discrete Semiconductor Modules
Lifecycle:	Obsolete



Images are for reference only

[Inquiry](#)

General Description

AD6623AS is a high-performance digital signal processing (DSP) chip developed by Analog Devices Inc. It is specifically designed for use in wireless communication systems, such as base stations, wireless infrastructure, and point-to-point microwave systems.

Key Features

High processing power: It has a high-speed parallel processing engine capable of executing up to 16 billion multiply-accumulate operations per second (16 GMACs/s).

Programmable functionality: The AD6623AS can be programmed to perform a wide range of signal processing functions, such as filtering, modulation, demodulation, coding, and decoding.

Low power consumption: It consumes relatively low power for its processing capabilities, making it suitable for battery-powered devices.

High dynamic range: It has a high dynamic range of up to 110 dB, which enables it to process weak and strong signals simultaneously.

Application

Wireless communication systems: It is used in base stations, wireless infrastructure, and point-to-point microwave systems to perform signal processing functions.

Defense and aerospace systems: It is used in radar systems, electronic warfare, and satellite communication systems.

Medical imaging: It is used in medical imaging systems, such as magnetic resonance imaging (MRI) and computed tomography (CT) scanners, for signal processing.



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