


SPLD GAL Family 8 Macro Cells 250MHz 3.3V 20-Pin PLCC

Manufacturer:	Lattice Semiconductor Corp
Package/Case:	PLCC20
Product Type:	Programmable Logic ICs
RoHS:	RoHS Compliant/Lead free 
Lifecycle:	Obsolete



Images are for reference only

[Inquiry](#)

General Description

The GAL16LV8D, at 3.5 ns maximum propagation delay time, provides the highest speed performance available in the PLD market. The GAL16LV8C can interface with both 3.3V and 5V signal levels. The GAL16LV8 is manufactured using Lattice Semiconductor's advanced 3.3V E2 CMOS process, which combines CMOS with Electrically Erasable (E2) floating gate technology. High speed erase times (<100ms) allow the devices to be reprogrammed quickly and efficiently.

The 3.3V GAL16LV8 uses the same industry standard 16V8 architecture as its 5V counterpart and supports all architectural features such as combinatorial or registered macrocell operations.

Unique test circuitry and reprogrammable cells allow complete AC, DC, and functional testing during manufacture. As a result, Lattice Semiconductor delivers 100% field programmability and functionality of all GAL products. In addition, 100 erase/write cycles and data retention in excess of 20 years are specified.



Recommended For You

[GAL16V8D-25LP](#)

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DIP20

[GAL16V8D-15QJ](#)

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PLCC20

[GAL16V8D-15LPN](#)

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DIP20

GAL16V8D-10LP

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DIP

GAL22V10D-15LJ

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PLCC28

GAL16V8D-15LJN

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PLCC20

GAL20V8B-15LP

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DIP24

GAL16V8D-25LJN

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PLCC

GAL16V8D-10LPN

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DIP

GAL16V8D-25QPI

Lattice Semiconductor Corp
DIP

GAL20V8B-15LPN

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DIP

GAL18V10B-20LP

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GAL22V10D-10LJN

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PLCC

GAL16V8D-25LJI

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PLCC20

GAL16LV8D-5LJ

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