

SPLD GAL Family 10 Macro Cells 80MHz 5V 28-Pin PLCC

Manufacturer:	Lattice Semiconductor Corp
Package/Case:	PLCC28
Product Type:	Programmable Logic ICs
Lifecycle:	Obsolete



Images are for reference only

[Inquiry](#)**General Description**

The GAL22V10, at 4ns maximum propagation delay time, combines a high performance CMOS process with Electrically Erasable (E2) floating gate technology to provide the highest performance available of any 22V10 device on the market. CMOS circuitry allows the GAL22V10 to consume much less power when compared to bipolar 22V10 devices. E2 technology offers high speed (<100ms) erase times, providing the ability to reprogram or reconfigure the device quickly and efficiently.

The generic architecture provides maximum design flexibility by allowing the Output Logic Macrocell (OLMC) to be configured by the user. The GAL22V10 is fully function/fuse map/parametric compatible with standard bipolar and CMOS 22V10 devices.

Unique test circuitry and reprogrammable cells allow complete AC, DC, and functional testing during manufacture. As a result, Lattice Semiconductor delivers 100% field programmability and functionality of all GAL products. In addition, 100 erase/write cycles and data retention in excess of 20 years are specified.

**Recommended For You**

GAL16V8D-25LP

Lattice Semiconductor Corp
DIP20

GAL16V8D-15QJ

Lattice Semiconductor Corp
PLCC20

GAL16V8D-15LPN

Lattice Semiconductor Corp
DIP20

GAL16V8D-10LP

Lattice Semiconductor Corp
DIP

GAL16V8D-15LJN

Lattice Semiconductor Corp
PLCC20

GAL20V8B-15LP

Lattice Semiconductor Corp
DIP24

GAL16V8D-25LJN

Lattice Semiconductor Corp
PLCC

GAL16V8D-10LPN

Lattice Semiconductor Corp
DIP

GAL16V8D-25QPI

Lattice Semiconductor Corp
DIP

GAL20V8B-15LPN

Lattice Semiconductor Corp
DIP

GAL18V10B-20LP

Lattice Semiconductor Corp
DIP20

GAL22V10D-10LJN

Lattice Semiconductor Corp
PLCC

GAL16V8D-25LJI

Lattice Semiconductor Corp
PLCC20

GAL16LV8D-5LJ

Lattice Semiconductor Corp
PLCC20

GAL16LV8D-3LJN

Lattice Semiconductor Corp
PLCC20