

## FPGA Configuration PROM Memory



Images are for reference only

[Inquiry](#)

**Manufacturer:** [AMD Xilinx, Inc](#)

**Package/Case:** TSSOP20

**Product Type:** Programmable Logic ICs

**RoHS:** RoHS Compliant/Lead free 

**Lifecycle:** Obsolete

## General Description

XCF02SVOG20C is a product code for a specific configuration of a Xilinx CoolRunner-II CPLD (Complex Programmable Logic Device) manufactured by Xilinx, a leading provider of programmable logic solutions.

## Key Features

2,160 usable gates

72 macrocells

64 I/O pins

20 MHz maximum operating frequency

1.8V to 3.3V supply voltage range

In-system programmable over JTAG

20-pin VQFN package

## Recommended For You

### [XCF128XFT64C](#)

AMD Xilinx, Inc

BGA

### [XC18V04VQ44I](#)

AMD Xilinx, Inc

QFP

### [XC17128EPD8I](#)

AMD Xilinx, Inc

DIP8

**XC1765ELSO8C**

AMD Xilinx, Inc

SOP8

**XC18V04VQ44C**

AMD Xilinx, Inc

QFP44

**XC18V01SO20C**

AMD Xilinx, Inc

SOP20

**XC18V04VQG44C**

AMD Xilinx, Inc

QFP

**XCF32PVOG48C**

AMD Xilinx, Inc

TSOP48

**XC18V01PCG20C**

AMD Xilinx, Inc

PLCC20

**XCF04SVO20C**

AMD Xilinx, Inc

TSSOP20

**XC2C256-7CPG132I**

AMD Xilinx, Inc

BGA132

**XCF04SVOG20C**

AMD Xilinx, Inc

TSSOP20

**XCF08PFS48C**

AMD Xilinx, Inc

BGA

**XC18V01VQ44C**

AMD Xilinx, Inc

TQFP44

**XC1765EPD8C**

AMD Xilinx, Inc

DIP8