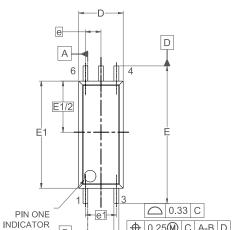




SOIC6 W LESS PIN 2 CASE 752AG ISSUE A

DATE 26 AUG 2021



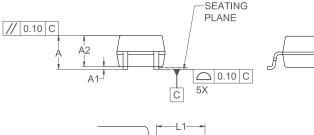
⊕ 0.25**M** C A-B D - 5 TIPS NOTES: UNLESS OTHERWISE SPECIFIED

- A) THIS PACKAGE DOES NOT CONFORM TO ANY STANDARD.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.

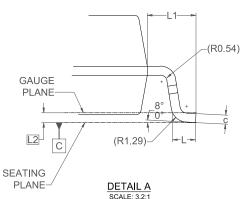
Α

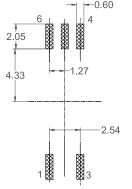
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS
- D) DRAWING CONFORMS TO ASME Y14.5M-1994

	MILLIMETER				
DIM	MIN.	NOM.	MAX.		
Α	_	_	2.95		
A1	0.10	0.20	0.30		
A2	2.45	2.55	2.65		
b	0.31	0.41	0.51		
С	0.19	0.22	0.25		
D	3.55	3.65	3.75		
Е	11.20	11.30	11.40		
E1	8.70	8.80	8.90		
E1/2	4.20 BSC				
е	1.27 BSC				
e1	2.54 BSC				
L	0.44	0.59	0.74		
L1	1.15	1.25	1.35		
L2	0.25 BSC				
θ	0°		8°		
→ -0.60					



5X b→





LAND PATTERN RECOMMENDATION

*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

DOCUMENT NUMBER:	98AON13752G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	SOIC6 W LESS PIN 2		PAGE 1 OF 1	

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.